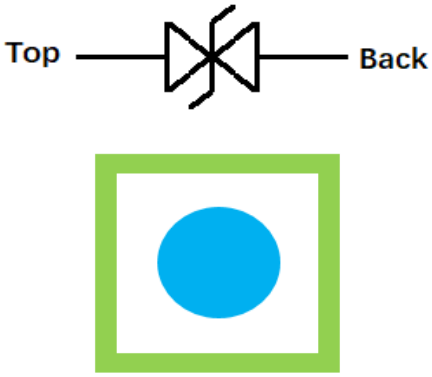
LM805ESDCP8-0.2

Item	Description	Unit
Wafer size	8	inch
Wafer thickness	150±10	μm
Die size (Include scribe line)	200*200	μm
Bond pad opening	Φ120	μm
Scribe line width	40	μm
Gross dies per wafer	700	K pcs
Top metal for wire bond	4μm AlSiCu	
Back side metal	TiNiAg	
Passivation thickness		μm

Die top-view & Circuit diagram



Absolute maximum ratings (TA=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak pulse current (8/20μs)	I_{PP}	8	A
ESD per IEC 61000−4−2 (air)	V_{ESD}	±30	kV
ESD per IEC 61000−4−2 (contact)	V_{ESD}	±30	kV
Operating temperature range	T_J	−55 to +125	°C
Storage temperature range	T_{stg}	−55 to +150	°C

Electrical characteristics (TA=25°C unless otherwise specified)

Parameter	Symbol	Test condition	Min	Typ	Max	Unit
Reverse working voltage	V_{RWM}				5	V
Breakdown voltage	V_{BR}	I_T = 1mA	5.5	6.2	7.5	V
Reverse leakage current	I_R	V_{RWM} = 5V			100	nA
Clamping voltage	V_C	I_{PP} = 1A (8 x 20μs)			8	V
Clamping voltage	V_C	I_{PP} = 8A (8 x 20μs)			14	V
Junction capacitance	C_J	V_R = 0V, f = 1MHz		15	18	pF

Absolute maximum ratings (TA=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak pulse power (8/20μs)	P_{PP}	104	W
Peak pulse current (8/20μs)	I_{PP}	8	A
ESD per IEC 61000−4−2 (air)	V_{ESD}	±30	kV
ESD per IEC 61000−4−2 (contact)	V_{ESD}	±30	kV
Operating Temperature Range	T_J	−55 to +125	°C
Storage Temperature Range	T_{stg}	−55 to +150	°C

Electrical characteristics (TA=25°C unless otherwise specified)

Parameter	Symbol	Test condition	Min	Typ	Max	Unit
Reverse working voltage	V_{RWM}				5	V
Breakdown voltage	V_{BR}	I_T = 1mA	5.5	6.2	7.5	V
Reverse leakage current	I_R	V_{RWM} = 5V		<10	100	nA
Clamping voltage	V_C	I_{PP} = 1A (8 x 20μS)		7	9	V
Clamping voltage	V_C	I_{PP} = 8A (8 x 20μS)		8.2	13	V
Junction capacitance	C_J	V_R = 0V, f = 1MHz		15	20	pF

NOTICE

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