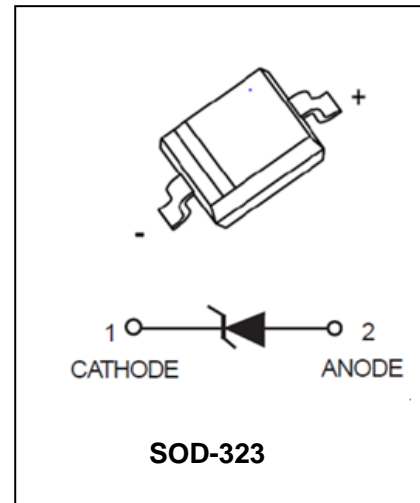


FEATURES

- Miniaturized package size for high density applications
- 2 % high precision regulated voltage stability
- High reliability chip and packaging process
- AEC-Q101 qualified

APPLICATIONS

- Mobile phone
- Portable device
- High precision computer motherboard



ORDERING INFORMATION

Type No.	Marking	Package Code
BZT52B5V6SQ	See Page2-3	SOD-323

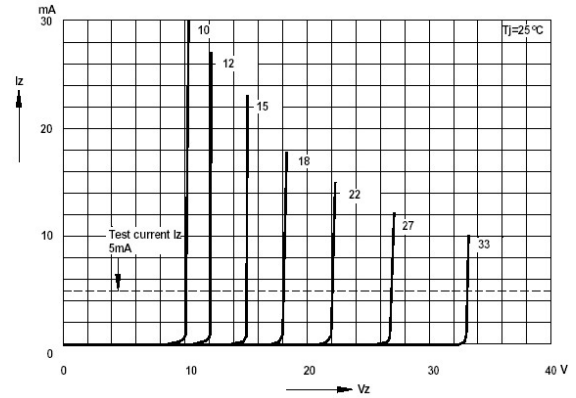
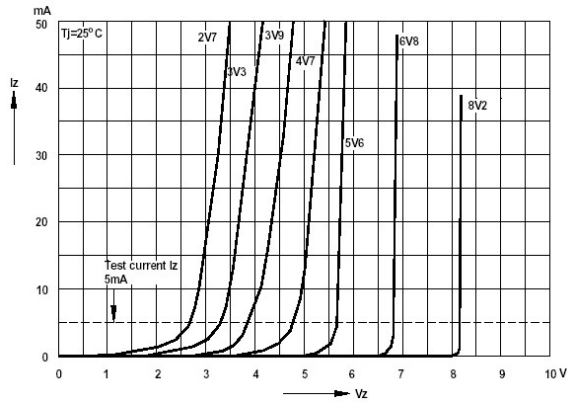
MAXIMUM RATING @ Ta=25°C unless otherwise specified

参数 Parameters	符号 Symbol	数值 Value	单位 Unit
功率消耗 Power Dissipation	Pd	200 ¹⁾	mW
正向压降 Forward Voltage @IF=10mA	Vf	0.9 ²⁾	V
存储温度 Storage temperature range	Ts	-65-+150	°C

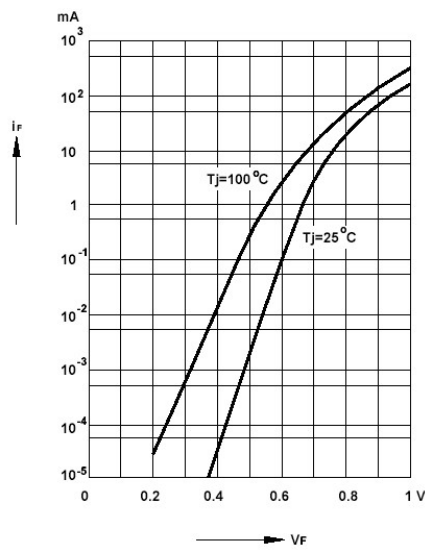
- 1) Device mounted on ceramic PCB: 7.6mm x 9.4mm x 0.87mm with pad areas 25mm²
- 2) Short duration test pulse used to minimize self-heating effect
- 3) f=1KHz

Device	Marking	Zener Voltage Range				Maximum Zener Impedance			Maximum Reverse Current		Typical Temperature coefficient @ IZTC=mV/°C		Test Current IZTC
		Vz@Izt			Izt	Zzt @Izt	Zzk @Izk	Izk	IR	VR	Min	Max	
		Nom(V)	Min(V)	Max(V)	mA	Ω	mA	uA	V	mA			
BZT52B5V6SQ	2W9	5.6	5.49	5.71	5	40	400	1.0	1	2.0	-2.0	2.5	5

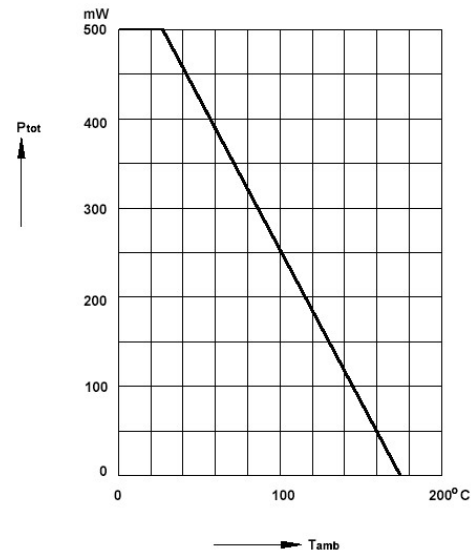
Breakdown characteristics at Tj=constant (pulsed)



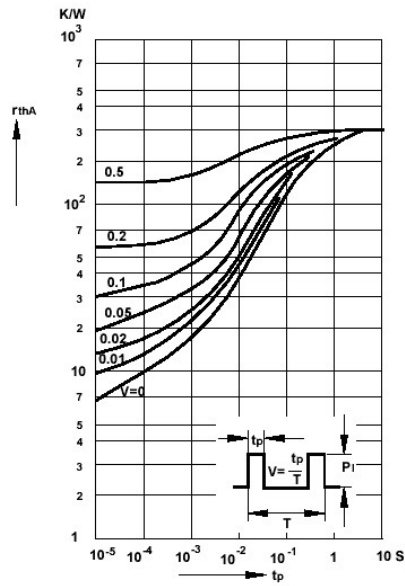
Forward characteristics



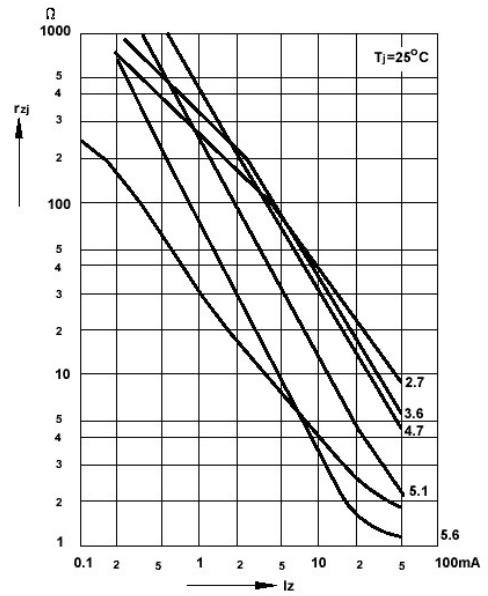
Admissible power dissipation versus ambient temperature



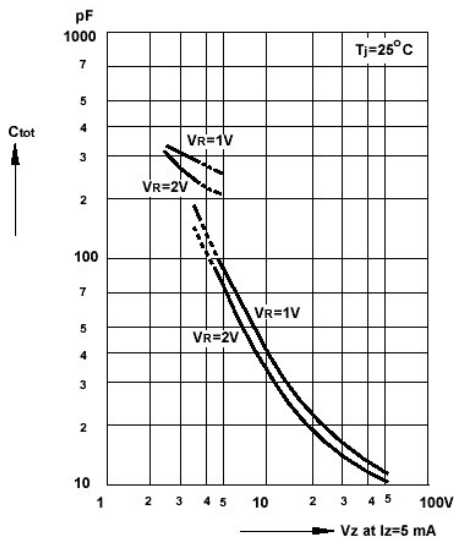
Pulse thermal resistance versus pulse duration



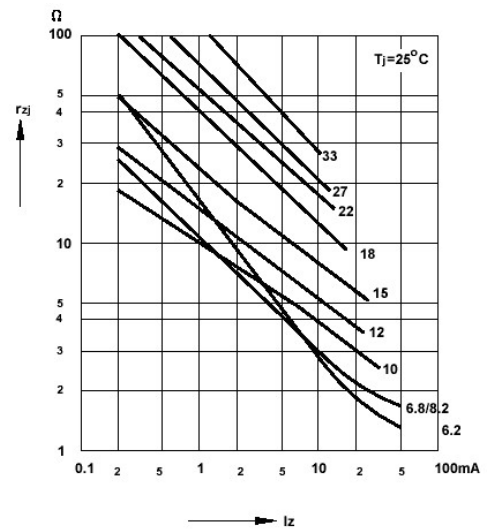
Dynamic resistance versus Zener current



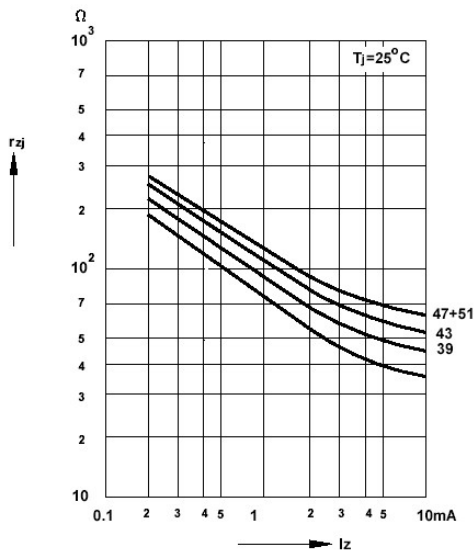
Capacitance versus Zener voltage



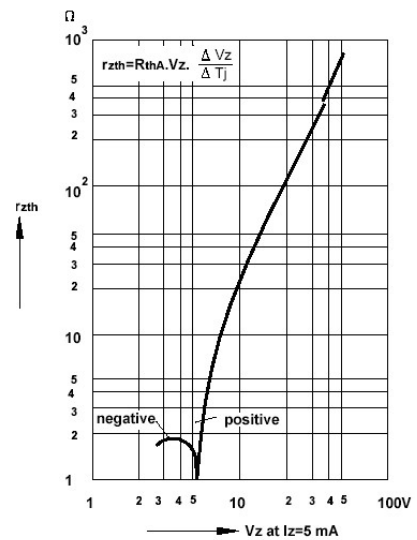
Dynamic resistance versus Zener current



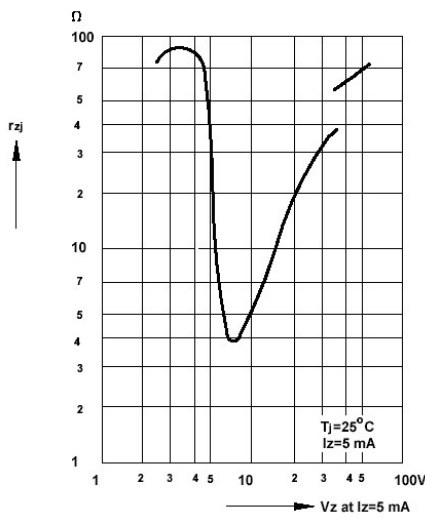
Dynamic resistance versus Zener current



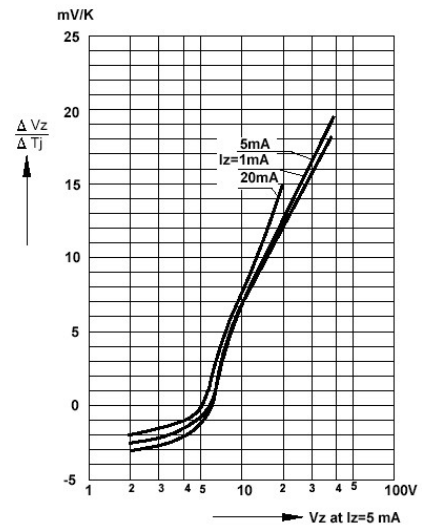
Thermal differential resistance versus Zener voltage



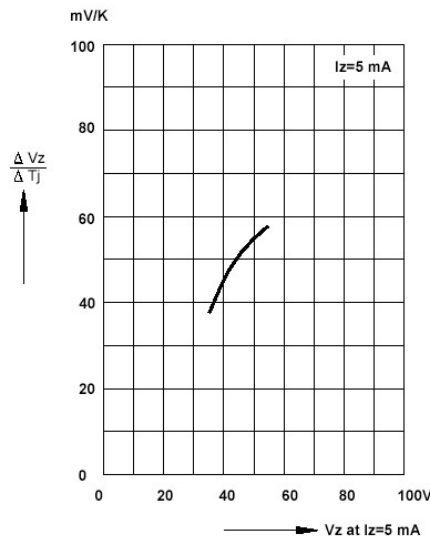
Dynamic resistance versus Zener voltage



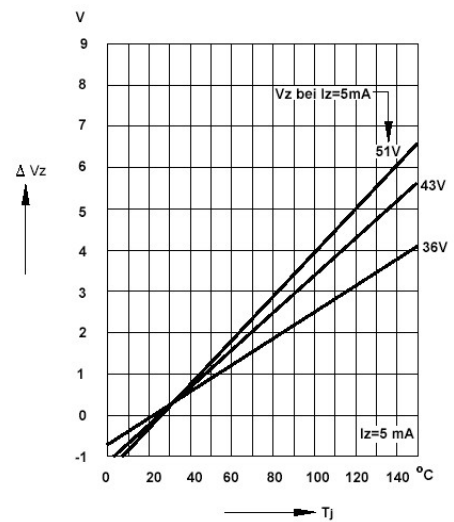
Temperature dependence of Zener voltage versus Zener voltage



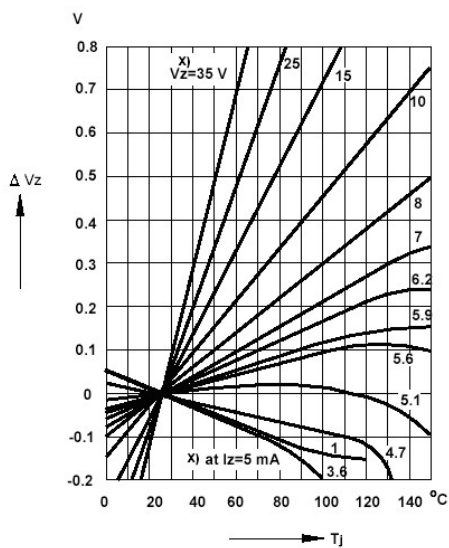
Temperature dependence of Zener voltage versus Zener voltage



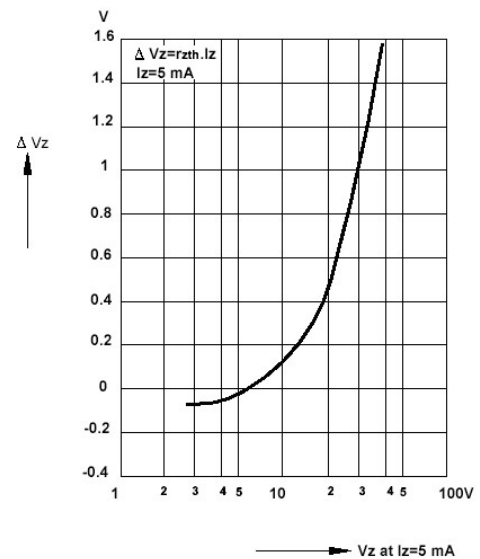
Change of Zener voltage versus junction temperature



Change of Zener voltage versus junction temperature

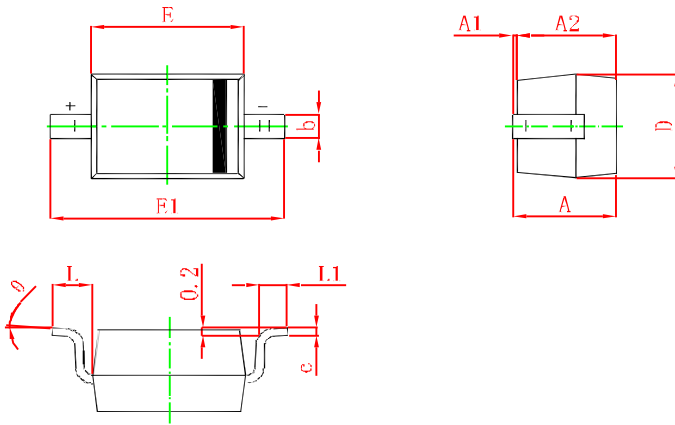


Change of Zener voltage from turn-on up to the point of thermal equilibrium versus Zener voltage



SOD-323 PACKAGE OUTLINE Plastic surface mounted package

SOD-323



Symbol	Min.(mm)	Max.(mm)
A		1.000
A1	0.000	0.100
A2	0.800	0.900
b	0.250	0.350
c	0.080	0.150
D	1.200	1.400
E	1.600	1.800
E1	2.500	2.700
L	0.475REF	
L1	0.250	0.400
θ	0°	8°

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